



2827

**TRANSMITTAL LETTER**  
(General - Patent Pending)

Docket No.  
**BUR920000228US1**

IN THE APPLICATION OF: Malinowski et al.

Serial No.	Filing Date	Examiner	Group Art Unit
09/683,027	11/9/01	Thai, Luan C.	2827

Title: **DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF INTEGRATED CIRCUITS**

**TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

Transmitted herewith is:

**Response to Restriction Requirement**

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in the above identified application.

No additional fee is required.

A check in the amount of \_\_\_\_\_ is attached.

The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. **09-0456(IBM)** as described below. A duplicate copy of this sheet is enclosed.

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Dated: *05/01/2002*

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I certify that this document and fee is being deposited on *5/1/02* with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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